**Lesson Plan**

**Name of the Assistant Professor**: Palvinder Singh

**Department:** Electronics & IT

**Subject:** Information Technology (I.T.)

**Lesson Plan:** 17 Weeks (July-November 2018)

|  |  |  |  |  |  |  |
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| **WEEK** | **DATE** | **Class: B.Sc(Hons)-IT****Sem: III****Paper: BSIT-505****Subject: Microprocessor and interfacing** **Days: : M-T-W (3 days**  | **Class: B.Sc(Hons)-IT****Sem: III****Paper: BSIT-501****Subject: Computer System Architecture Days: M-T-W (3 days)** | **Class: B.Sc(Hons)-IT****Sem: V****Paper: BSIT-105****Subject: Analog Communication** **Days: M-T-W (3 days)** | **Class: B.Sc(Hons)-IT****Sem: V****Paper: BSIT-106****Subject: Fundamental of computer** **Days: Th-F-S (3 days)** |  |
| I | **16-07-2018** | Introduction to 8086 microprocessor, concept of Even and Odd Memory, | Basic Computer Organisation and Design: Instruction Codes,  | **Analog Modulation:-** Communication Model, Modulation |  |  |
| **17-07-2018** | Semiconductor memory interfacing | Computer registers,Computer Instructions,  | AM |  |  |
| **18-07-2018** | Examples of Static RAM interfacing | Timing and Control,  |  Mathematical equation of AM, sideband of AM  |  |  |
| **19-07-2018** |  |  |  | **Introduction to computer system:-**What is a Computer, Evaluation & Generation of computers |  |
| **20-07-2018** |  |  |  | Evaluation & Generation of computers |  |
| **21-07-2018** |  |  |  | Evaluation & Generation of computers |  |
| **22-07-2018** | **SUNDAY SUNDAY SUNDAY** |
| II | **23-07-2018** | Dynamic RAM interfacing | Instruction Cycle,  | **Power Relation of AM** | ,  | **-** |
| **24-07-2018** | I/O interfacing | Memory referenceinstructions,  | Numerical Problems |  | **-** |
| **25-07-2018** | Introduction to 8255 | Input-Output and Interrupt,  | Mathematical equation of FM, sideband of AM |  | **-** |
| **26-07-2018** |  |  |  | Applications and characteristics of computers |  |
| **27-07-2018** |  |  |  | elements of Computer (Hardware, Software), |  |
| **28-07-2018** |  |  |  | common input & output devices (Basic idea).  |  |
| **29-07-2018** | **SUNDAY SUNDAY SUNDAY** |
| III | **30-07-2018** | CWR | Design of Basic computer,  |  Mathematical equation of FM, sideband of AM  | **-** | **-** |
| **31-07-2018** | **MARTYRDOM DAY OF SHAHEED UDHAM SINGH HOLIDAY** |
| **01-08-2018** | Keyboard Interfacing |  |  Numerical Problems | **-** | **-** |
| **02-08-2018** |  |  |  | Types of Computers: Micro, Mini, Main frame,  |  |
| **03-08-2018** |  |  |  | super computers, Block diagram of PC architecture,  |  |
| **04-08-2018** |  |  |  | concept of files and directories |  |
| **05-08-2018** | **SUNDAY SUNDAY SUNDAY** |
| IV | **06-08-2018** | interfacing Examples | Design of Basic computer | Demodulation, PM (Quantitatively & Qualitatively |  |  |
| **07-08-2018** | I/O mapped and Memory Mapped scheme | Design ofaccumulator logic |  Relationship between FM & PM  |  |  |
| **08-08-2018** | Introduction to A/D  | Revision |  |  |  |
| **09-08-2018** |  |  |  | Sequential and direct access devices,  |  |
| **10-08-2018** |  |  |  | Magnetic disk |  |
| **11-08-2018** |  |  |  | Floppy Disk,  |  |
| **12-08-2018** | **SUNDAY SUNDAY SUNDAY** |
| V | **13-08-2018** |  D/A convertor and its IC | doubt | Revision | **-** | **-** |
| **14-08-2018** | Interfacing A/D data converters,  | Register Transfer and Microoperations: Register Transfer Language (RTL),  | **Class Test** | **-** | **-** |
| **15-08-2018** | **INDEPENDENCE DAY** |
| **16-08-2018** |  |  |  | Winchester Disk, Mass Storage,  |  |
| **17-08-2018** |  |  |  | Optical Disk,  |  |
| **18-08-2018** |  |  |  | Magnetic Bubble Memory |  |
| **19-08-2018** | **SUNDAY SUNDAY SUNDAY** |
| VI | **20-08-2018** | interfacing D/A converters , various Example , Interconnection topologies, | registertransfer, Bus and Memory Transfers,  | **Doubt session** | **-** | **-** |
| **21-08-2018** | Interconnection topologies | Arithmetic Microoperations, LogicMicrooperations,  | Analog to digital conversion: Pulse Modulation system | **-** | **-** |
| **22-08-2018** | **IDUL JUHA** |
| **23-08-2018** |  |  |  | Charged Coupled Device |  |
| **24-08-2018** |  |  |  | Cache Memory |  |
| **25-08-2018** |  |  |  | Storage hierarchy. |  |
| **26-08-2018** | **SUNDAY SUNDAY SUNDAY** |
| VII | **27-08-2018** | software aspects of multimicroprocessor systems,  | Shift Microoperations, Arithmetic Logic Shift Unit | , the sampling theorem for low pass  | **-** | **-** |
| **28-08-2018** | Numeric Processor 8087 | **Revision**  |  Band pass signals,  | **-** | **-** |
| **29-08-2018** | Numeric Processor 8087 | **Class Test** | PAM, channel Bandwidth for PAM, sampling rate, natural sampling | **-** | **-** |
| **30-08-2018** |  |  |  | Revision  |  |
| **31-08-2018** |  |  |  | Revision  |  |
| **01-09-2018** |  |  |  | Class Test |  |
| **02-09-2018** | **SUNDAY SUNDAY SUNDAY** |
| VIII | **03-09-2018** | JANMASHTAMI |
| **04-09-2018** | **Revision** | Central Processing Unit: General registers Organization | quantization of signals,  |  |  |
| **05-09-2018** | **Revision** | Stack Organization,  | quantization errors  |  |  |
| **06-09-2018** |  |  |  | **Problems Related to Test** |  |
| **07-09-2018** |  |  |  | What is Software, Relationship between Software and Hardware,. |  |
| **08-09-2018** |  |  |  | Types of softwares: System Software (Meaning and its type),  |  |
| **09-09-2018** | **SUNDAY SUNDAY SUNDAY** |
| IX | **10-09-2018** | Doubt Session | Instructionformats | Revision |  |  |
| **11-09-2018** | Doubt Session | Addressing Modes | **Class Test** |  |  |
| **12-09-2018** | **TEEJ** |
| **13-09-2018** |  |  |  | Application software,  |  |
| **14-09-2018** |  |  |  | Acquiring Software,  |  |
| **15-09-2018** |  |  |  | Software Development Steps,  |  |
| **16-09-2018** | **SUNDAY SUNDAY SUNDAY** |
| X | **17-09-2018** | **Class test** |  |  |  |  |
| **18-09-2018** | Introduction to stack, stack structure of 8086,. | Data Transfer and Manipulation,  | The PCM System |  |  |
| **19-09-2018** | interrupts and interrupts service routines, | Program Control | Bandwidth requirements of PCM,  |  |  |
| **20-09-2018** | [MUHARRAM](https://www.bankbazaar.com/indian-holiday/muharram-holiday.html) |
| **21-09-2018** |  |  |  | Firmware, Middleware |  |
| **22-09-2018** |  |  |  | **Revision**  |  |
| **23-09-2018** | **SUNDAY SUNDAY SUNDAY** |
| XI | **24-09-2018** | interrupt cycle of 8086, non maskable interrupt, maskable interrupt  | Program interrupt | Noise in PCM System,. |  |  |
| **25-09-2018** |  interruptprogramming, passing parameters to procedures,  | Reduced Instruction Set Computer (RISC), CISC characteristics | Companding |  |  |
| **26-09-2018** | handling programs of size more than 64K , MACROS | **Revision**  | DPCM,  |  |  |
| **27-09-2018** |  |  |  | Class Test  |  |
| **28-09-2018** |  |  |  | Class Test related Problems |  |
| **29-09-2018** |  |  |  | Windows as an interface,  |  |
| **30-09-2018** | **SUNDAY SUNDAY SUNDAY** |
| XII | **01-10-2018** | timings and delays | Class Test  | Delta Modulation System , |  |  |
| **02-10-2018** | MAHATMA GANDHI JAYANTI |
| **03-10-2018** | timings and delays | Microprogrammed Control: Control memory;  | Noise in Delta Modulation System,  |  |  |
| **04-10-2018** |  |  |  | Introduction to desktop, accessories,  |  |
| **05-10-2018** |  |  |  | internet: definition, basic services  |  |
| **06-10-2018** |  |  |  | Internet uses |  |
| **07-10-2018** | **SUNDAY SUNDAY SUNDAY** |
|  XIII | **08-10-2018** | Salient features 80286 | Microprogrammed Control: Control memory;  | Comparison of PCM & DM System | **-** |  |
| **09-10-2018** | Salient features 80386 | address sequencing | **Class Test** | **-** |  |
| **10-10-2018** | **MAHARAJA AGRASEN JAYANTI** |
| **11-10-2018** |  |  |  | multimedia: meaning, multimedia  |  |
| **12-10-2018** |  |  |  | multimedia components,  |  |
| **13-10-2018** |  |  |  |  multimedia applications  |  |
| **14-10-2018** | **SUNDAY SUNDAY SUNDAY** |
| XIV | **15-10-2018** |  Salient features 80586 | address sequencing | Elements of digital communication |  |  |
| **16-10-2018** | Salient features , Pentium 4 | microprogramsequencer | Analysis & design of communication system,  |  |  |
| **17-10-2018** | **Revision** |  |  |  |  |
| **18-10-2018** |  |  |  | Word: Introduction of Word,  |  |
| **19-10-2018** | [**DUSSHERA**](https://www.bankbazaar.com/indian-holiday/dussehra-holiday.html) |
| **20-10-2018** |  |  |  | creating, editing a document |  |
| **21-10-2018** | **SUNDAY SUNDAY SUNDAY** |
| XV | **22-10-2018** | **Revision** |  |  | **-** | **-** |
| **23-10-2018** | Sessional Test  | microprogramsequencer | bit rate, baud rate | **-** | **-** |
| **24-10-2018** | **MAHARSHI VALMIKI BIRTHDAY** |
| **25-10-2018** |  |  |  | modifying and formatting a document,  |  |
| **26-10-2018** |  |  |  | speller in word,  |  |
| **27-10-2018** |  |  |  | Creating and using macros. |  |
| **28-10-2018** | **SUNDAY SUNDAY SUNDAY** |
| XVI | **29-10-2018** | Doubt Session regarding sessional test | Design of Control Unit | transmission rate, echo canceller, Characteristics of digital Communication | **-** | **-** |
| **30-10-2018** |  Introduction RISC processor, Design Issue  | Sessional Test | Bandwidth requirement, transmission errors, echo suppressor,  | **-** | **-** |
| **31-10-2018** | Feature of RISC processor | **Doubt of sessional test** | Sessional Test | **-** | **-** |
| **01-11-2018** | **HARYANA DAY** |
| **02-11-2018** |  |  |  | **Sessional Test** |  |
| **03-11-2018** |  |  |  | Problems related to sessional Test |  |
| **04-11-2018** | **SUNDAY SUNDAY SUNDAY** |
|  XVII | **05-11-2018** | Last question Paper Discussion | Last question Paper Discussion | Last question Paper Discussion | Last question Paper Discussion |  |
| **06-11-2018** | **DIWALI BREAK (VACATION-1)** |
| **07-11-2018** |
| **08-11-2018** |
| **09-11-2018** |
| **10-11-2018** |
| **11-11-2018** |
|  XVIII | **12-11-2018** |
| **13-11-2018** |