Name of the Teacher: Palvinder Singh Class: B.Sc IT VTH SEM (BSIT-505)

**Lesson Plan**

|  |  |  |  |
| --- | --- | --- | --- |
| **S No** | **Period** | **Topics to be Covered** | **Academic Activity to be Organized** |
|  | **21-22 July 2017** | Introduction to 8086 microprocessor, concept of Even and Odd Memory, Semiconductor memory interfacing | **Discussion** |
|  | **01-09 Aug 2017** | Examples of Static RAM interfacing, Dynamic RAM interfacing, I/O interfacing | **PPT** |
|  | **16-23 Aug 2017** | Introduction to 8255,CWR, Keyboard Interfacing, interfacing Examples | **Open Book Test** |
|  | **29-30 Aug 2017** | I/O mapped and Memory Mapped scheme | **Discussion** |
|  | **04-18 Sep 2017** | Introduction to A/D and D/A convertor and its IC, Interfacing A/D data converters, interfacing D/A converters , various Examples | **PPT** |
|  | **19-25 Sep 2017** | Interconnection topologies,  software aspects of multimicroprocessor systems, Numeric Processor 8087 | **PPT** |
|  | **27 Sep, 2017** | Class Test |  |
|  | **03-11 Oct, 2017** | Introduction to stack, stack structure of 8086, interrupts and interrupts service routines,  interrupt cycle of8086, non maskable interrupt, maskable interrupt (INTR), interrupt  programming, passing parameters to procedures, handling programs of size more than  64K, MACROS, timings and delays. | **PPT** |
|  | **09-11 Oct, 2017** | Salient features 80286, 80386, 80586, Pentium 4 | **PPT** |
|  | **16 -04 Nov, 2017** | Basic features of RISC Processors and  Design issues of RISC Processors. | **PPT** |

**Topics of Assignments/ Class Tests to be given to the Students:**

|  |  |
| --- | --- |
| **Assignment 1** | **Difference between 8085 and 8086, Dynamic RAM interfacing** |
| **Assignment 2** | **Difference between Salient features 80286, 80386 and Pentium V** |
| **Class Test** | **Interfacing Examples of Static RAM** |