**Name of the Teacher:** Magandeep Kaur **Class:** B.Sc-Elect-II-SEM-III

**Subject:** Opamp and Linear Integrated Circuits-I  **Paper:** I

**Lesson Plan**

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| **S No** | **Period** | **Topics to be Covered** | **Academic Activity to be Organized** |
|  | **17-31 July 2017** | **Unit3:** DC Coupled Amplifier, Single and Double Ended differential Amplifier, Differential Gain. | Group discussion  |
|  | **01-31 Aug 2017** | **Unit3:** Differential Gain, Common-mode gain, CMRR, Ideal Opamp, Feedback in inverting & non-inverting configuration, Buffer, Summing & Difference amplifier. | Group discussion  |
|  | **01-30 Sept 2017** | **Unit4:** Offset voltages and currents, input bias current, input offset voltage, error introduced by offset voltage, integrating and differentiating circuit using Opamp, multiplication, division, Schmitt trigger, Active filters using Opamp(Ist order).  | ------ |
|  | **01-31 Oct 2017** | **Unit1:** Basics of IC technology, Monolithic Fabrication Tecchniques, Classification of IC’s.**Unit2:** Transistors for Monolithic circuits, monolithic diodes. | Power-Point Presentation  |
|  | **01-13 Nov 2017** | **Unit2:** Integrated resistors, Integrated capacitors and inductors, JFET, MOSFET, Monolithic circuit layout. | Power-Point Presentation  |

**Topics of Assignments/ Class Tests to be given to the Students:**

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| **Assignment 1** | Opamp and its characteristics, summing & difference amplifier. |
| **Assignment 2** | Fabrication Techniques. |
| **Class Test** | **Test 1:** Opamp and its applications.**Test 2:** Integrator, Differentiator, Multiplication & Division**Test 3:** Fabrication techniques. |